Registration form for
One Week Refresher Course
on

Application of AI Techniques in Electrical Engineering

(Under TEQIP Phase – III)

18th - 23th November 2019

1. Name: Prof./Dr./Mr./Ms._____
2. Age (years)_____
3. Qualifications_____
4. Designation_____
5. Department _____
6. Institution_____
7. Experience (in years)

Teaching_____

Industry_____

8. Address: _____

_____

Phone:_____

Email:_____

9. D.D. Particulars Amount Rs.: Date:

D.D. No: Bank:

Date Signature of the applicant

Signature of the Sponsoring Authority

(Brochure can be downloaded from our website: www.uceou.edu)

Speakers Details

Speakers are drafted from institutions like IITs, NITs and other reputed universities to share their experiences in building your research knowledge.

Registration Fee

Engineers from Industry	: Rs. 5000/-
Teachers from Academic Institutions	: Rs.3500/-
Research Scholars / Students	: Rs.2000/-

The Payment of Registration fee is to be made by a Demand Draft in favour of " Head, Department of Electrical Engineering, UCE (A), OU ".

Filled-in application forms may also be sent by e-mail with a scanned copy of the DD to nagalakshmi.eedou@gmail.com. Original DD should be submitted at the time of Registration.

Important Dates

Last Date for
Receipt of Applications: 11th November, 2019

Contact Person
Mrs. G.V.Naga Lakshmi
Assistant Professor
9492047114
E-mail: nagalakshmi.eedou@gmail.com

Accommodation & Transport

Accommodation will be provided on request for the outside Male and Female participants in the hostel on payment basis (with nominal charges).

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Organised by
DEPARTMENT OF ELECTRICAL ENGINEERING
University College of Engineering (Autonomous)
Osmania University, Hyderabad – 500 007

In Association with

DEPARTMENT OF ELECTRICAL ENGINEERING
Veer Surendra Sai University of Technology
Burla, Orissa.

About the course:

Artificial Intelligence (AI) is the intelligence of machines that aims to create it. It is defined as the "study and design of intelligent agents", where an intelligent agent is a system that pursues its environment and takes actions that maximize its challenges of success. Several problems in electrical engineering cannot be solved by conventional techniques based on several constrains which may not be feasible all the time. In these situation AI techniques like Fuzzy logic ANN and several meta heuristic techniques like Genetic algorithm, Particle swarm optimization, Bacterial foraging optimization and Bio geography based optimization techniques are used. All these techniques are widely used for load flow studies, optimal reactive power dispatch, economic dispatch, optimal sizing and placement of DG's, optimal protection coordination of directional over current relays, power electronic converters, etc.

This one week refresher course aims at exposing the participants to Artificial intelligence techniques like ANN, Fuzzy logic, Genetic algorithm, Particle swarm optimization, Bacterial foraging optimization and Bio geography based optimization techniques and their applications to electrical engineering. An intensive hands on sessions on the above techniques is being organized during the workshop. The workshop helps the participants in upgrading and enhancing their technical knowledge.

About VSSUT, Burla, Orissa

This legendary University is named after Veer Surendra Sai, the great Indian freedom fighter and sacrificed his life fighting against the British. Situated at the foothill of world famous Hirakud dam, the University is established on 12th Aug. 1956, at Burla. The University occupies nearly 300 acres of prime land in Burla. The primary objective of the college is to produce engineers who can manage Hirakud Dam. VSSUT provides its students with modern educational facilities while retaining traditional values, as well as using its strong industrial contacts to mold young, talented individuals who can compete in the global arena. The aim of VSSUT is to rank among leading universities globally. Consequently, VSSUT's mission is to educate individuals to be competitive not only in India, but all over the world.

About the Department of Electrical Engineering, VSSUT, Burla

The Department of Electrical Engineering is one among the first branches to be instituted in 1956.While retaining its strength in traditional areas of engineering, the department grew with time, reflecting the needs of a changing society and established new areas of teaching & research in electrical engineering. The department has AICTE accreditation. The department has state of the art infrastructure established in order to promote a congenial academic environment to impart quality education.

About the Dept.t of Electrical Engineering, University College of Engineering, Osmania University:

The University College of Engineering was established in the year 1929 and celebrated its platinum Jubilee in 2004. The college became autonomous in 1994. The Department of Electrical Engineering was started in the year 1949 and celebrated its Golden Jubilee during the academic year 1999-2000. The post-graduate course in Electrical Machines was started in 1966. With a view to provide support to industry, two more diversifying post graduate programs were introduced in 1971 for both part-time and full-time students. The Ph.D program was introduced in 1972. The Department is well equipped with Circuits Lab, Computer Lab, Control Lab, Integrated Circuits Lab, Microprocessor Lab, Power Electronics Lab, Power Systems Lab and Drives Lab. The Department has procured latest software, machines and other sophisticated equipment under TEQIP.

Chief Patron
Sri Arvind Kumar, IAS
Govt. of Telangana
(I/C) Vice Chancellor, Osmania University.

Patron
Prof. M. Kumar
Principal, UCE (A), Osmania University.

Dean
Prof. P.V.N.Prasad
Faculty of Engineering, Osmania University.

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Prof. G. Mallesham
EED, UCE (A), OU.
Dr. Banaja Mohanthi
VSSUT, Burla.

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